

What is claimed is:

- Sub A*
1. A data processing device comprising:
a plurality of connection terminals for being
5 individually supplied with signals including processing
data, a clock signal, and a reset signal, and drive
electric power;
at least one radio antenna for receiving said
signals and said drive electric power as one radio
10 wave;
a data processing circuit switchable between a
terminal mode in which only the signals supplied to
said connection terminals are effective and an RF mode
in which only the radio wave supplied to said radio
15 antenna is effective, said data processing circuit
being supplied with said drive electric power and said
signals; and
a mode selecting circuit for setting said data
processing circuit to said RF mode by default in
20 response to said drive electric power starting to be
supplied, and for switching to said terminal mode in
response to the clock signal and the reset signal which
are applied to corresponding ones of said connection
terminals.

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2. A data processing device according to claim
1, wherein said mode selecting circuit has mode
maintaining means for maintaining said terminal mode
until the supply of said drive electric power is
5 stopped.

3. A data processing device according to claim
1, wherein said mode selecting circuit comprises:
clock counting means for counting clock pulses of
10 the clock signal supplied in response to said drive
electric power starting to be supplied; and
input deciding means for outputting a switching
signal to switch said data processing circuit to said
terminal mode when said clock counting means has
15 counted a predetermined number of clock pulses.

4. A data processing device according to claim
3, wherein said input deciding means has data output
means for outputting said reset signal as said
20 switching signal when said clock counting means has
counted a predetermined number of clock pulses.

5. A data processing device according to claim
3, wherein said mode selecting circuit has mode
25 maintaining means for applying said switching signal
output by said input deciding means as a dummy clock

signal to said clock
loop.

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